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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known Application Number 10/770,435 Filing Date February 3, 2004 First Named Inventor Shi Art Unit 2816 **Examiner Name** Luu, A. RAMB-01015US1 Attorney Docket Number

(Use as many sheets as necessary) 2 Sheet

			U. S. PATENT I	DOCUMENTS	
Examiner Initials*	Cite No. <sup>1</sup>	Document Number  Number-Kind Code <sup>2 (# known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
<del></del>		<sup>US-</sup> 6504438	01-07-2003	Chang et al.	
		<sup>US-</sup> 6809600 B2	10-26-2004	Chang et al.	
		∪s-6928128 B1	08-09-2005	Sidiropoulos	
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document  Country Code <sup>3</sup> Number <sup>4</sup> 'Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T⁵	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449B/PTO				Complete if Known		
				Application Number	10/770,435	
IN	<b>FORMATION DI</b>	ISCLO	SURE	Filing Date	February 3, 2004	
ST	TATEMENT BY	APPLI	CANT	First Named Inventor	Shi	-
				Art Unit	2816	
	(Use as many sheets as	s necessary)	)	Examiner Name	Luu, A.	
She	et 2	of	2	Attorney Docket Number	RAMB-01015US1	

NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*						
- 1		CHANG et al., "A 0.4-4Gb/s CMOS Quad Transceiver Cell using On-chip Regulated Dual-Loop PLLS," 2002 Symposium on VLSI Circuits Digest of Technical Papers				
	•	SIDIROPOULOS et al., "A Semidigital Dual Delay-Locked Loop," IEEE Journal of Solid-State Circuits, Vol. 32, No. 11, November 1997	***********			
		MANEATIS, "Low-Jitter Process-Independent DLL and PLL Based on Self-Biased Techniques," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996				
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